

# ABSTRACT

In a multiple power source semiconductor integrated circuit that is manufactured using a process which generates a large leakage current, supply of power to a function block that is not being used is stopped to reduce unnecessary power consumption. A multiple power source semiconductor integrated circuit (1) includes first to fourth function blocks (11) to (14) that are supplied with power from first to fourth power supply circuits (3) to (6), respectively, and a power supply control circuit (40) that controls supply of power by the first to fourth power supply circuits (3) to (6) under the control of a microcomputer as the first function block (11). The power supply control circuit (40) halts the supply of power to the first to fourth function blocks (11) to (14) when receiving prescribed data from the first function block (11), and restarts the supply of power when receiving a first or second interrupt signal (55) or (56) from outside.